

#3

1204.44657X00

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): Yasushi SHIMADA et al.

Serial No.: (not yet assigned)

Filed: January 18, 2005

For: Multilayer Wiring Board, Manufacturing Method Thereof,  
Semiconductor Device, and Wireless Electronic Device

**INFORMATION DISCLOSURE STATEMENT**  
**UNDER 37 CFR 1.97 & 1.98**

January 18, 2005

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In the matter of the above-identified application, applicant(s) are submitting herewith a copy of the documents listed in the attached form equivalent to Form PTO-1449 for the Examiner's consideration.

This information disclosure statement is being submitted with the new application.

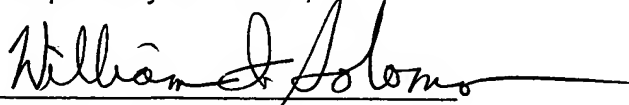
To the extent that the documents listed on the attached form equivalent to Form PTO-1449 are not in the English language, the requirement of 37 CFR 1.98(a)(3) for a concise explanation of the relevance is satisfied by

an English Abstract and/or the discussion of these documents in the specification.

It is respectfully requested that this information disclosure statement be considered by the Examiner.

Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account No. 01-2135 (1204.44657X00) and please credit any excess fees to such deposit account.

Respectfully submitted,



William I. Solomon  
Registration No. 28,565  
ANTONELLI, TERRY, STOUT & KRAUS, LLP

WIS/anp  
Attachments  
(703) 312-6600

PTO/SB/08A (08-03)

Approved for use through 07/31/2006. OMB 0651-0031  
U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449A/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  <i>(use as many sheets as necessary)</i>				<b>Complete if Known</b>	
				Application Number	1204.44657X00
				Filing Date	January 18, 2005
				First Named Inventor	Yasushi SHIMADA et al.
				Art Unit	
				Examiner Name	
Sheet	1	of	2	Attorney Docket Number	1204.44657X00

**U.S. PATENT DOCUMENTS**

[illegible]

## FOREIGN PATENT DOCUMENTS

Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)				
		09-148746	06/06/1997	JAPAN		
		10-013036	01/16/1998	JAPAN		
		2001-068858	03/16/2001	JAPAN		
		01-189999	07/31/1989	JAPAN		
		07-183665	07/21/1995	JAPAN		
		06-085462	03/25/1994	JAPAN		
		05-055079	03/05/1993	JAPAN		

Examiner Signature		Date Considered	
-----------------------	--	--------------------	--

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional). 2 See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. 3 Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). 4 For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. 5 Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. 6 Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

Substitute for form 1449/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)				<b>Complete if Known</b>		
				Application Number		20052000
				Filing Date		January 18, 2005
				First Named Inventor		Yasushi SHIMADA et al.
				Group Art Unit		
				Examiner Name		
Sheet	2	of	2	Attorney Docket Number		1204.44657X00

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		Electrical Properties of Buried Capacitor in Zero X-Y Shrinkage Multilayer Ceramic Substrate, Osama Inoue; Seiichi NAKATANI; Junichi KATO; and Yutaka TAGUCHI vol. 4, No. 2(2001) pages 145-149	
		Examination of the Built-in STO Film Capacitor Process Technology on Resin-Molded High Frequency MCM and the Capacitor Frequency Characteristics, Hiroji YAMADA and Kiichi YAMASHITA col. 4 No.7 (2001) page 590-596	
		Integration of Thin Film Passive Circuits Using High/Low Dielectric Constant Materials, P. Chahal; A. Haridass; A. Pham; R.R. Tummala; M.G. Allen; m. Swaminathan and J. Laskar, 1997 Electronic Components and Technology Conference pages 739-744	
		National Center for Manufacturing Sciences Pages 3-1-3-6	

Examiner Signature		Date Considered	
--------------------	--	-----------------	--

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Applicant's unique citation designation number (optional). \* Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 120 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing the this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2